



EFW

PATENT
P56909

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Cheol-Hee MOON

Serial No.: 10/669,748

Examiner: *To be assigned*

Filed: 25 September 2003

Art Unit: 2879

For: PLASMA DISPLAY PANEL

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes, and provides copies of the following art references. Under 37 C.F.R. §1.98(a)(2) however, copies of U.S. patent reference(s) are not provided.

FOREIGN PATENT REFERENCE:

- Japanese Patent Publication No. 2000-182523 to Bun, entitled *PLASMA DISPLAY PANEL*, published on 30 June 2000 (with English abstract)
- Japanese Patent Publication No. 2000-243303 to Amano, entitled *STRUCTURE FOR BACK SIDE SUBSTRATE OF DISCHARGE TYPE DISPLAY DEVICE*, published on 8 September 2000 (with English abstract)
- Japanese Patent Publication No. 2000-77002 to Toyoda, *et al.*, entitled *PLASMA*

DISPLAY PANEL AND MANUFACTURE THEREOF, published on 14 March 2000
(with English abstract)

- Japanese Patent Publication No. 2001-345054 to Sano, *et al.*, entitled *PLASMA DISPLAY DEVICE*, published on 14 December 2001 (with English abstract)
- Japanese Patent Publication No. 11-126562 to Fujii, *et al.*, entitled *FIXING STRUCTURE OF PARALLEL WIRE ELECTRODE GROUP AND CONNECTING METHOD BY USING IT*, published on 11 May 1999 (with English abstract)
- Japanese Patent Publication No. 2001-222958 to Amamiya, entitled *PLASMA DISPLAY PANEL*, published on 17 August 2001 (with English abstract)
- Japanese Patent Publication No. 9-306368 to Fujii, entitled *GAS DISCHARGE PANEL, FITTING METHOD OF METAL WIRE AND FORMING METHOD OF GAS DISCHARGE PANEL*, published on 28 November 1997 (with English abstract)

DISCUSSION

Bun JP'523, states that this plasma display panel includes upper and lower substrates 21, 22 disposed opposite to each other, pairs of upper electrodes 23 spaced apart from each other and parallel formed on a lower surface of the upper substrate 21, a first dielectric layer 24 spread over the lower surface of the upper substrate 21 so as to bury the upper electrodes 23 therein, barrier ribs 28 provided at fixed intervals on the lower substrate 22 for limiting discharge spaces, lower electrodes 26 made up of conductive wire provided in the discharge spaces on the lower substrate 22 so as to make a right angle with the upper electrodes 23, and fluorescent layers 29 spread within the discharge spaces. The display panel capable of securing quality reliability and having various deformation structures can be simply manufactured by a much simplified process compared with those in the past.

Amano JP'303 states that a plurality of parallel barrier plates 3 are formed on a plate glass by the chemical etching method or sand blast method to form a back glass substrate 1. After the barrier plates 3 are formed on the back glass substrate 1, phosphors 4 are applied to the inner wall faces of grooves surrounded by the barrier plates 3 by the screen printing method. The phosphors 4 are applied to the whole inner wall faces of the barrier plates 3. Metal wires or metal plates are linearly machined by the etching method to form electrodes 2. The electrodes 2 may be covered on the surface with a dielectric substance such as a glass material or a negative electrode material with high secondary electron emissivity, e.g. lanthanum hexaborid, magnesium oxide or lithium oxide, as required for operation.

Toyoda, et al., JP'002 relates that a pair of substrates is arranged so that a discharging space is provide. A plurality of beltlike partitions 29 are arranged parallelly on either the back side or front side of the substrate in order to separate the discharging space. A phosphor layer ((28R), 28G, (28B)) is provided within a narrow recess between the partitions 29. A wall-like protrusion 51, which has a height lower than the partition 29 and capable of increasing the area for forming the phosphor layer, is provided at least in the region forming a discharging portion within the narrow recess between the partitions 29. The phosphor layer is formed within the recess between the partitions 29 including the wall-like protrusion 51.

Sano, et al., JP'054 relates that this plasma display device has opposing linear edges separated by a predetermined distance with each other, and display electrodes are constituted by a pair of electrodes whose width is reduced as it goes away from the edge, and by insulation walls having inner walls along outer circumferential parts of display electrodes, cells to emit light by being excited by display electrodes are formed and installed.

Fujii, et al., JP'562 relates that besides the panel display part, the arrangement is furnished with a front face base board 10 and a back face base board 12, and either of them is equipped with

at least two guide regions, the first 100 and the second 200, having grooves 106 and 206 to admit insertion of a parallel wire electrode group and a deformation region 300 which is located apart from the first 100 and second guide regions 200 in straight relation and has a projection 302 for elastically curvedly deforming the parallel wire electrode group between the first and second guide regions.

Amamiya JP'958 relates that the plasma display panel has harrier ribs 35a extending in a column direction and barrier ribs 35 to demarcate the discharge space S in each discharge cell C in row and column directions by using barrier ribs 35b extending in a row direction between the front face glass base plate 10 and rear face glass base plate 13. The horizontal ribs 35b between the discharge cells C along the rows adjacent to each other are separated by the gaps SL parallel with the row direction. The gaps SL and discharge cells C adjacent to each other in the row direction communicate each other through the groove 11Aa formed in a raised dielectric layer 11A.

Fujii JP'368 relates that a partition 19 provided in an upper surface of 11a of a back substrate 11 is constituted by a first/second partition part 19a, 19b, and, in a side of a front substrate 13 of the partition 19, a wire fitting groove 23 for holding a metal wire 21 as a cathode with a fixed space is formed. A 42-6 alloy wire as the metal wire 21 is fitted to the wire fitting groove 23, to be provided with a fixed space as a plurality of rows. In the case of forming a panel thus provided, first, a wire feed part, while fitting the metal wire 21 to a groove concerned to a first row, is rotated by one turn passing the center of the groove concerned to the first groove and in a plane vertical to an upper surface of the back substrate 11. Similar work is performed by a prescribed number of times.

Pursuant to 37 CFR §1.97(d), the undersigned attorney hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three (3) months prior to the filing of the statement.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

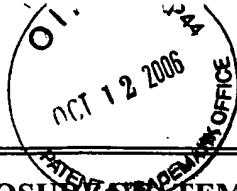
No fee is incurred by this Statement.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'R. E. Bushnell', is written over a horizontal line.

Robert E. Bushnell
Reg. No.: 27,774
Attorney for the Applicant

1522 "K" Street, N.W., Suite 300
Washington, D.C. 20005
Area Code: (202) 408-9040
Folio: P56909
Date: 10/12/06
I.D.: REB/nm

**INFORMATION DISCLOSURE STATEMENT****PTO-1449 (PAGE 1 OF 1)**

SERIAL NUMBER 10/669,748

DOCKET NO. P56909

APPLICANT Cheol-Hee MOON

FILING DATE 25 September 2003

GROUP 2879

U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS**TRANSLATION**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	JP 2000-182523	06/2000	JAPAN			Abstract	
	JP 2000-243303	09/2000	JAPAN			Abstract	
	JP 2000-77002	03/2000	JAPAN			Abstract	
	JP 2001-345054	12/2001	JAPAN			Abstract	
	JP 11-126562	05/1999	JAPAN			Abstract	
	JP 2001-222958	08/2001	JAPAN			Abstract	
	JP 9-306368	11/1997	JAPAN			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.